

11/5/05

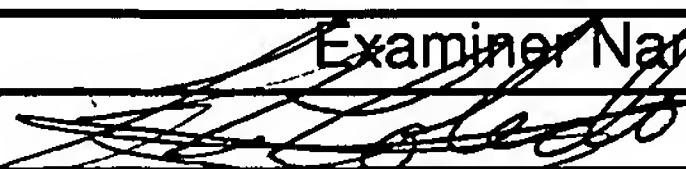
## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

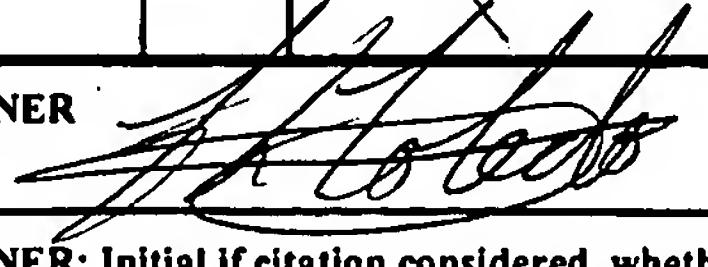
Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	A SUSPENSION FOR FILLING VIA HOLES IN SILICON AND METHOD FOR MAKING THE SAME						
Application Number :	10/707,693						
Confirmation Number:							
First Named Applicant:	JON CASEY						
Attorney Docket Number:	FIS920030334US1						
Art Unit:	2823						
Examiner:	F. TOLEDO						
Search string:	( 3752685 or 5337475 or 5614043 or 6143421 or 6329065 or 6346317 or 6426126 ).pn						
<b>US Patent Documents</b>							
<b>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</b>							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	1	3752685	1973-08-14	Hidemasa Honda, et al.	1	1	
2	2	5337475	1994-08-16	Aoude, et al.			
3	3	5614043	1997-03-25	Ritland, et al.			
4	4	6143421	2000-11-07	Ritland, et al.			
5	5	6329065	2001-12-11	Ishida, et al.			
6	6	6346317	2002-02-12	Ritland, et al.			
7	7	6426126	2002-07-30	Conover, et al.			

**Signature**

Examiner Name	Date
	7/18/05

O / P INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)			Docket Number (Optional) <b>FIS920030334US1</b>		Application Number <b>10/707,693</b>	
			Applicant(s) <b>JON A. CASEY, ET AL.</b>			
			Filing Date <b>10/707,693</b>	Group Art Unit <b>2823</b>		
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
<b>FOREIGN PATENT DOCUMENTS</b>						
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						YES
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>						
		IBM TECHNICAL DISCLOSURE BULLETIN, "ACTIVE SILICON CHIP CARRIER" July 1972, PP 657-657				
EXAMINER 		DATE CONSIDERED <b>7/8/05</b>				